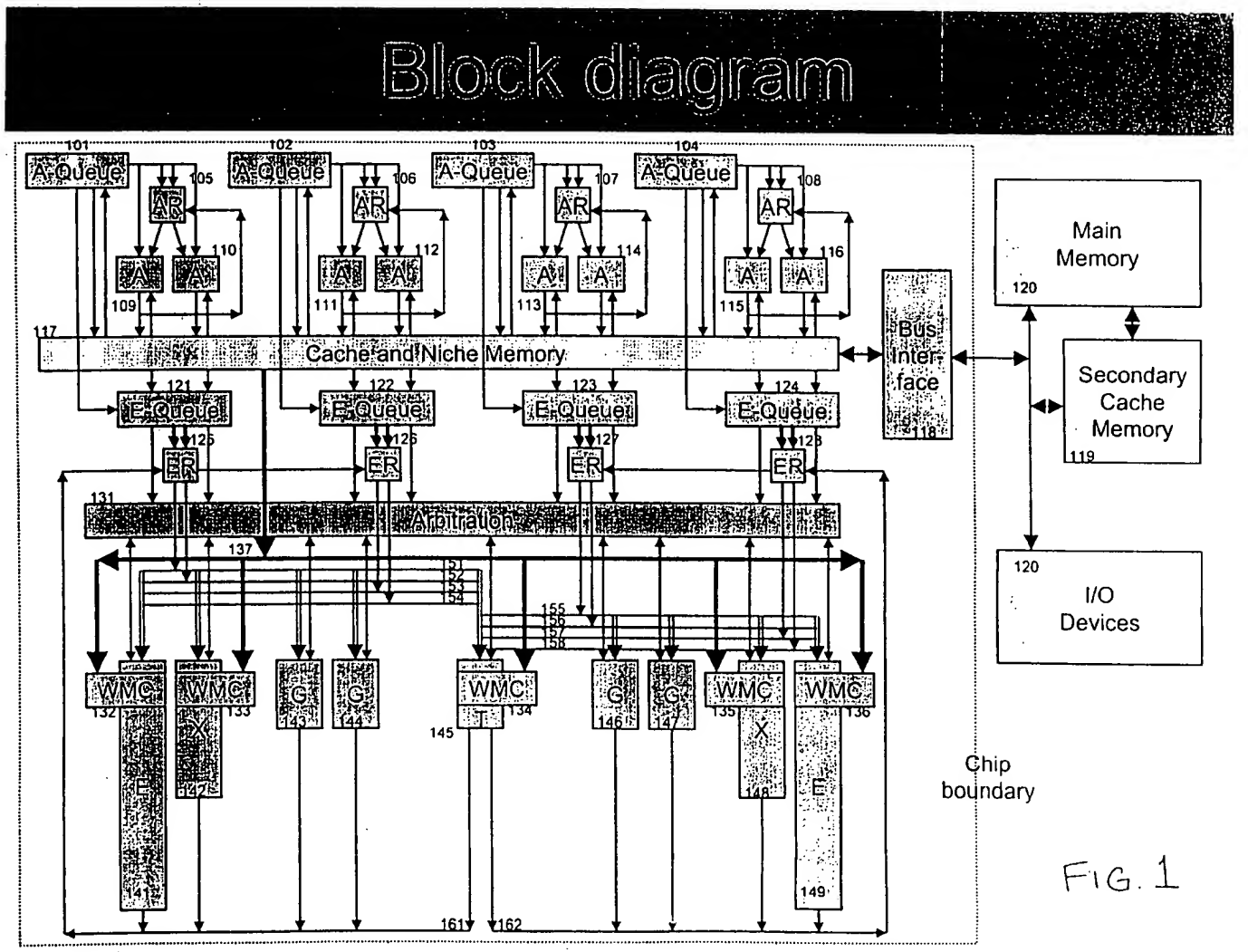


FIG. 1

Wide multiply matrix

- $rd_{128} = m[rc]_{(128*64/size)} * rb_{128}$

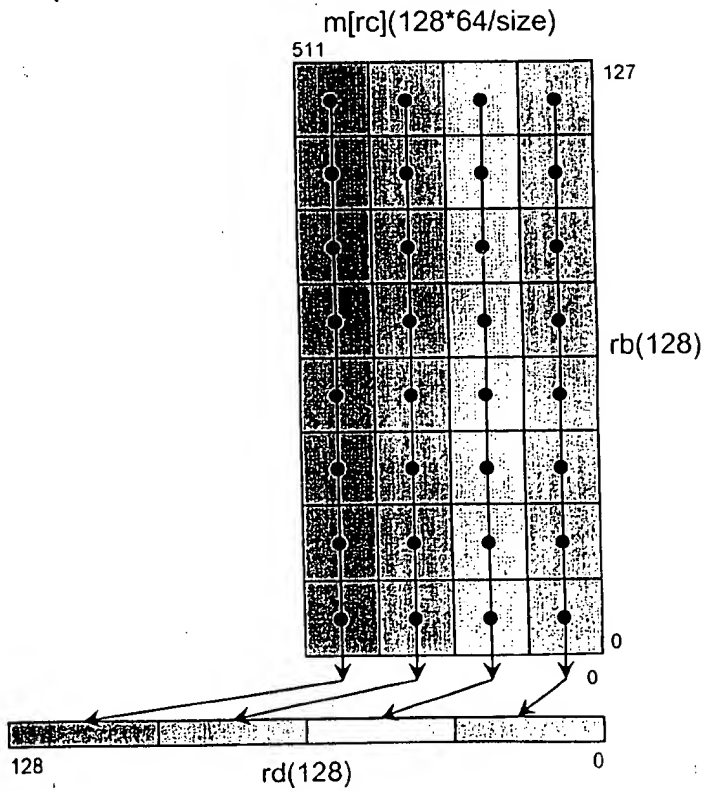


FIG. 2

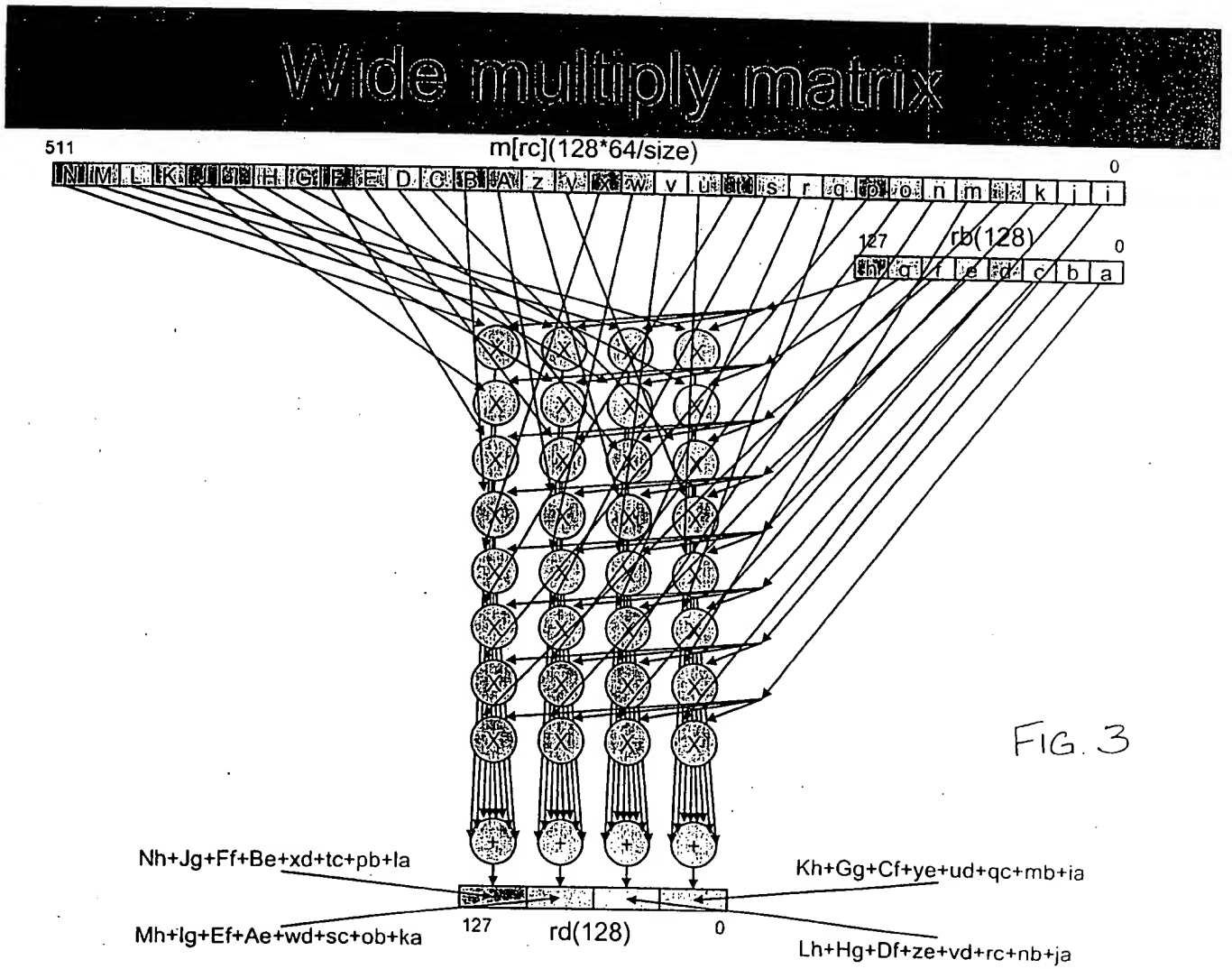


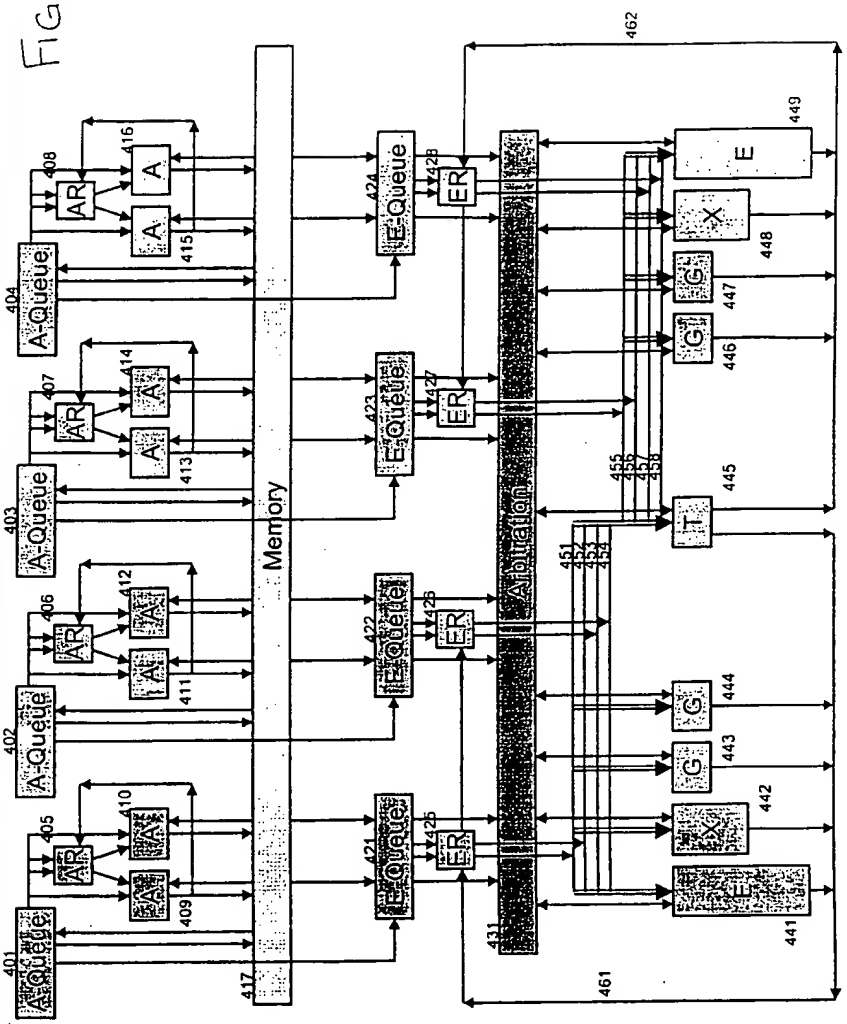
FIG. 3

PRINT OF DRAWINGS
AS ORIGINALLY F[illegible]D

EP 0 752 645 A3

SMT + DAE

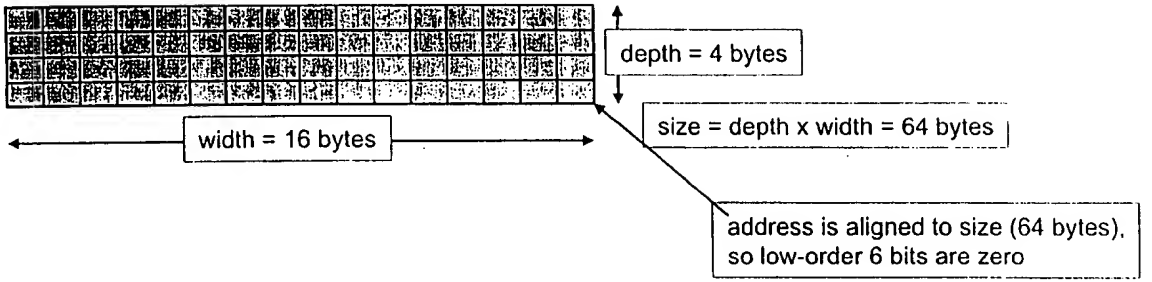
FIG. 4

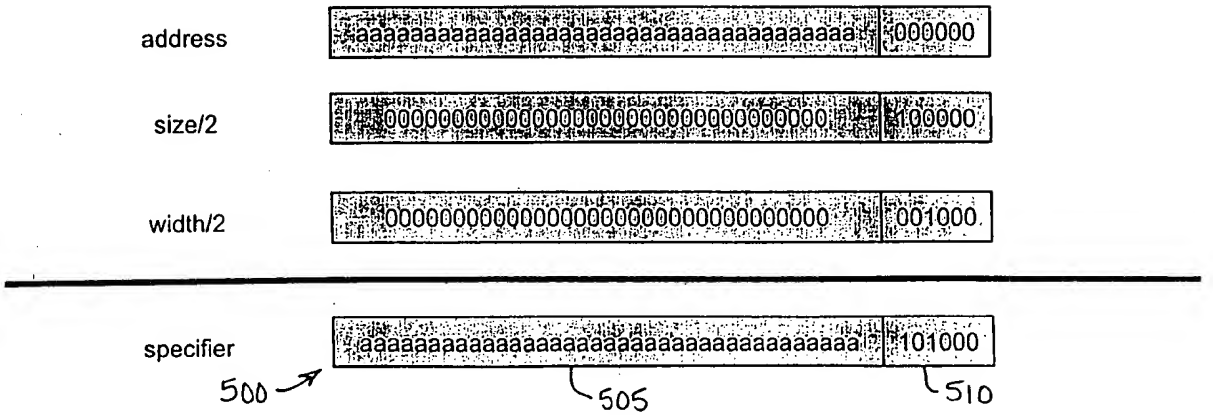


Wide operand specifier

- specifier=address+(size/2)+(width/2)

FIG. 5





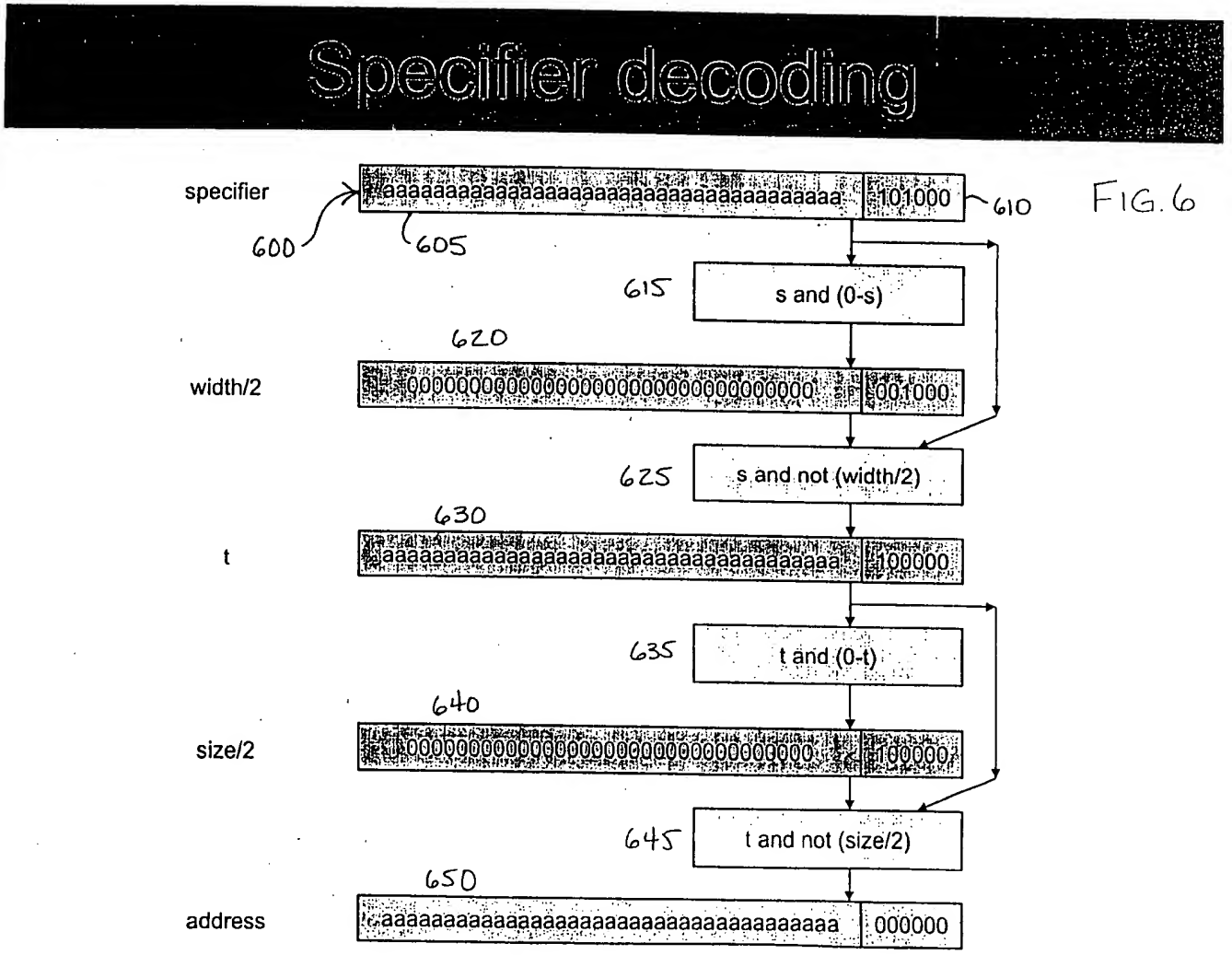


FIG. 6

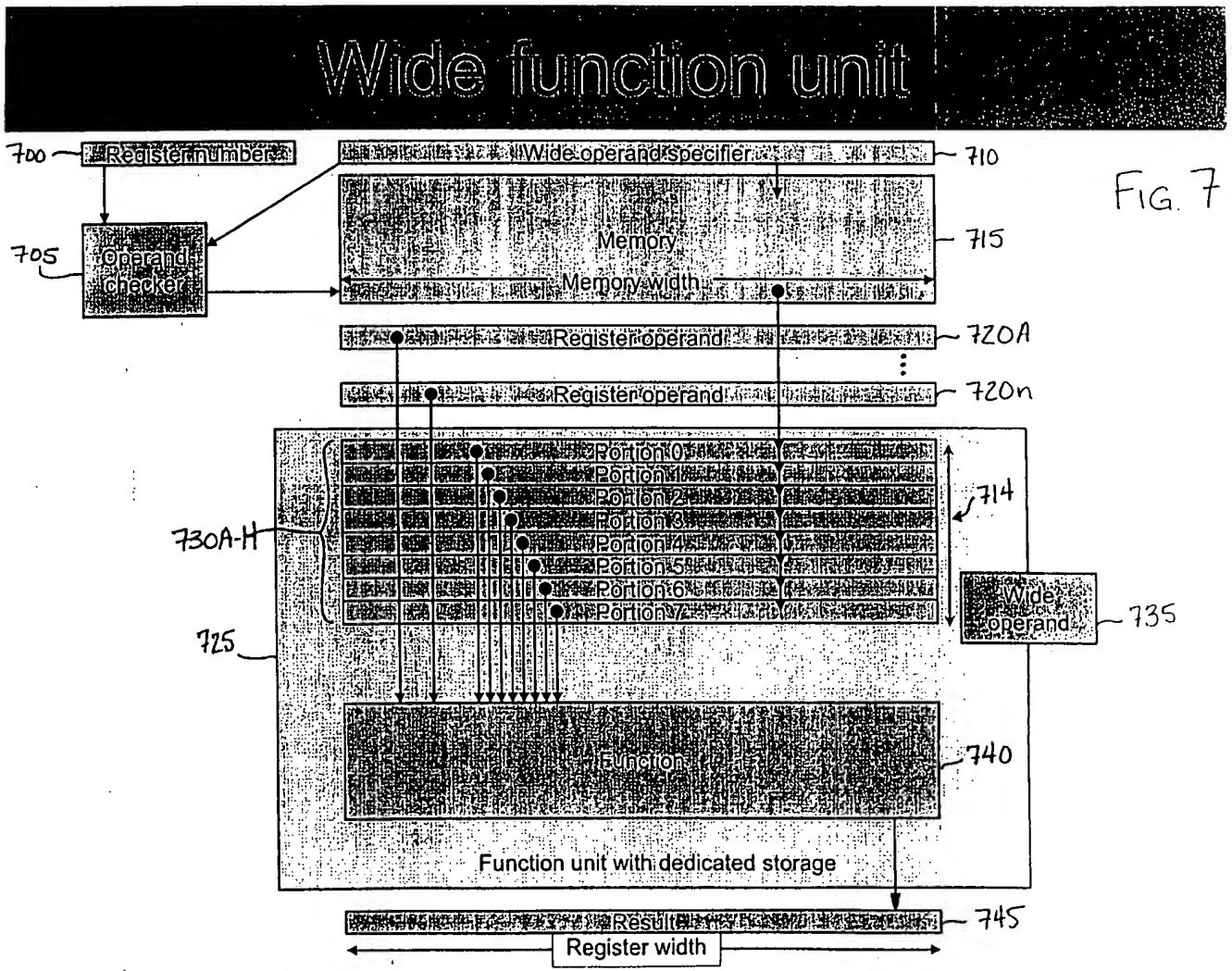


FIG. 7

Wide MicroCache control

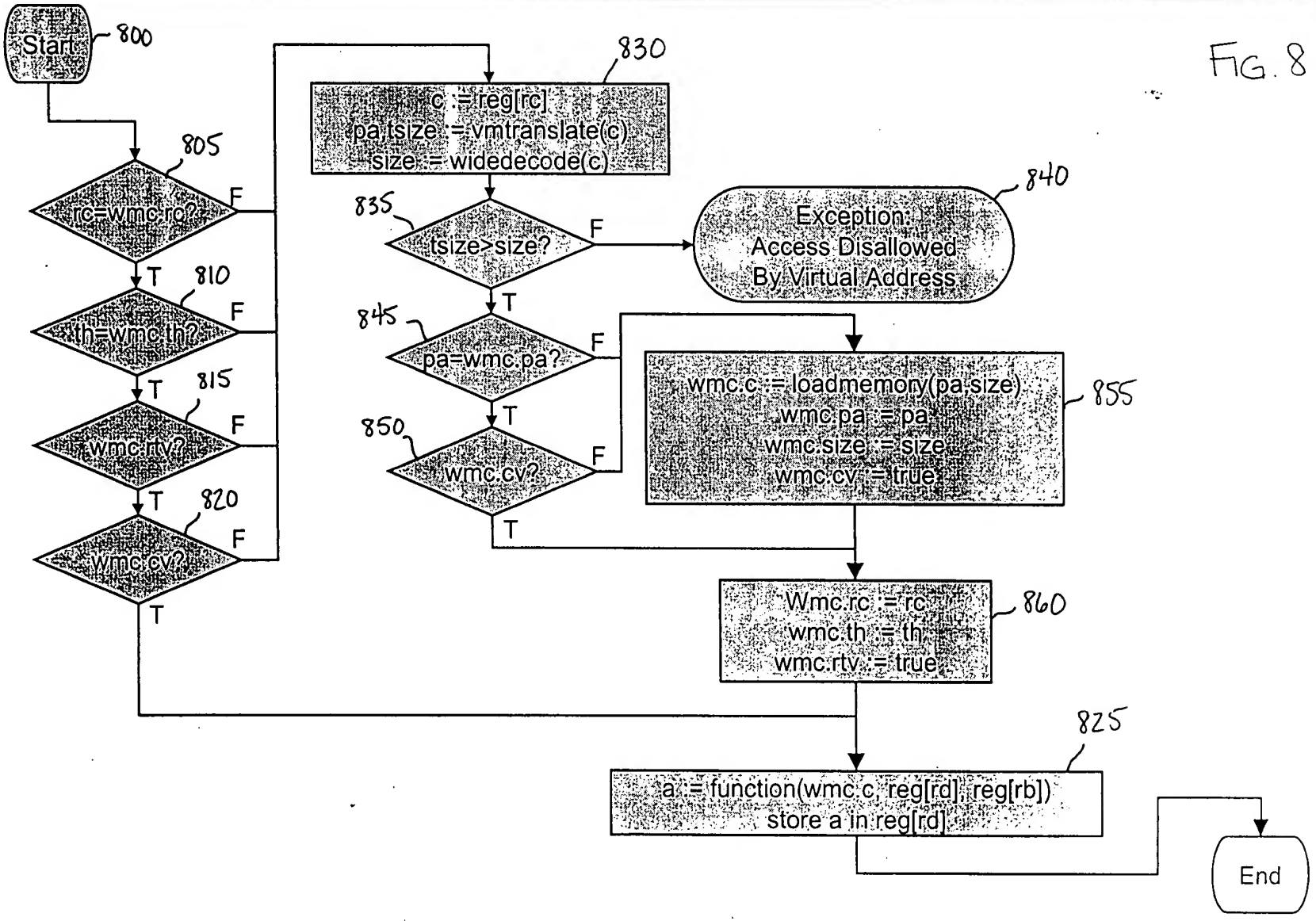


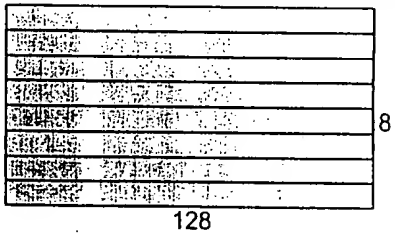
FIG. 8

PRINT OF DRAWINGS AS ORIGINALLY

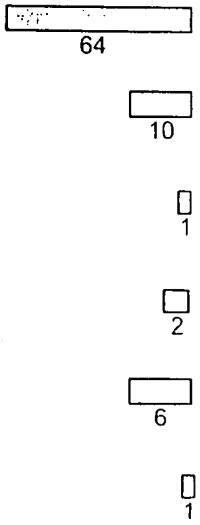
Wide MicroCache data structures

FIG. 9

- wmc.c contents

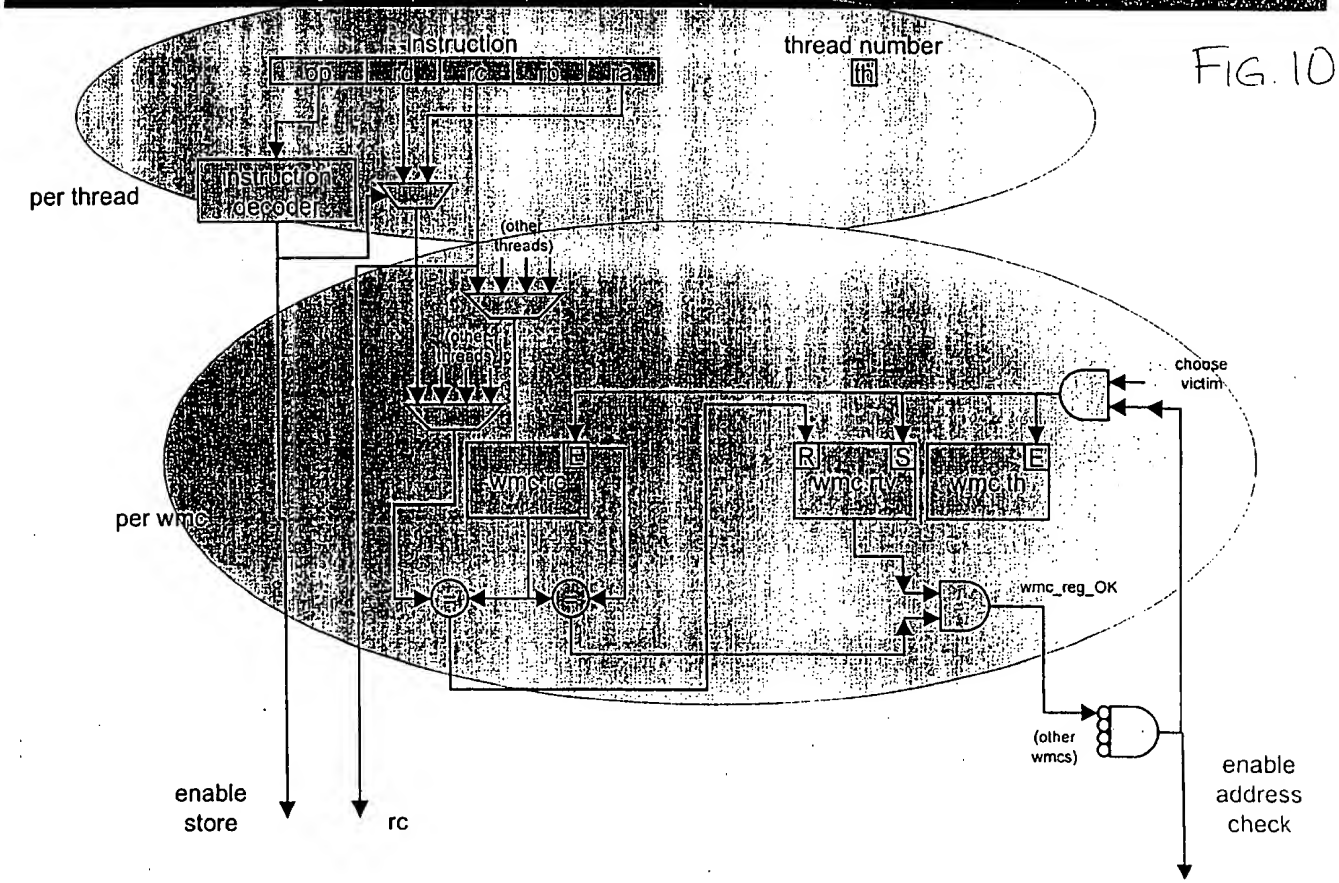


- wmc.pa - physical address
- wmc.size - size of contents
- wmc.cv - contents valid
- wmc.th - thread last used
- wmc.reg - register last used
- wmc.rtv - register & thread valid



Wide MicroCache control (1)

FIG. 10



Wide MicroCache control (2)

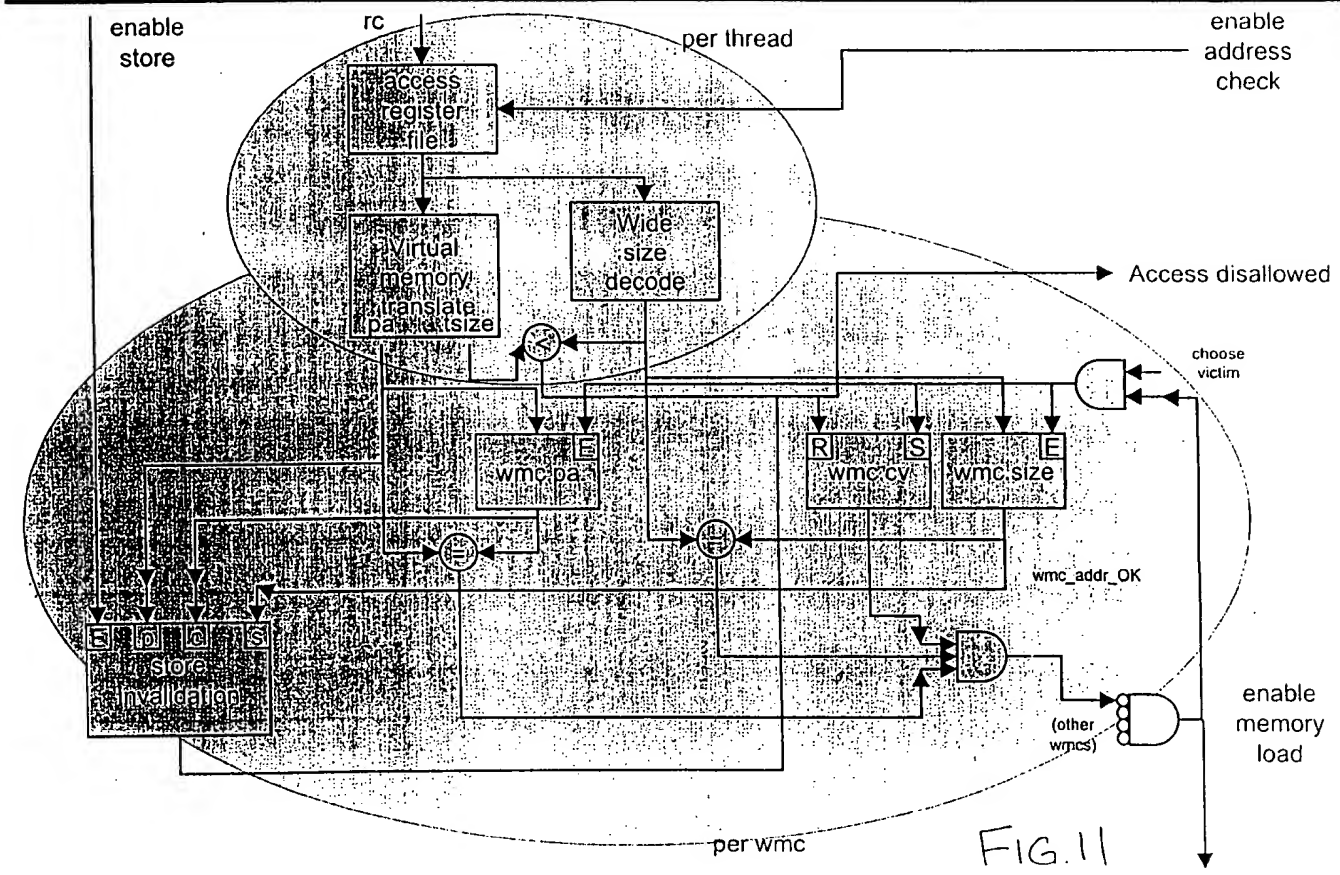


FIG.11